Call for Papers

Austrochip is an annual workshop on microelectronics and integrated circuits in Austria and neighboring countries. The 31st edition will be organized by the Institute of Electronics at Graz University of Technology.

Topics of the workshop include but are not limited to:

- Analog, mixed-signal, and RF integrated circuits
- Digital circuits, filters, DSPs, asynchronous designs
- FPGA design and reconfigurable hardware
- Design methodology, system-level design, gigascale circuits, network-on-chip
- Embedded systems, low-power designs, RF systems, security aspects
- Verification and testing, signal integrity, device modelling, timing analysis, reliability simulation, EMC, ESD
- Emerging technologies, nano CMOS process, sub-threshold circuits, sensors, organic and biomedical electronics
- Case studies and prototyping

Submissions from academia as well as industry are welcome. Especially, PhD-Students are invited to present their research work related to topics of Austrochip, but also excellent MSc-Theses will be considered. Submissions will be double-blind-reviewed. Accepted papers will be made available electronically via a download link and we plan to publish all papers written in English through IEEE Xplore (with indexing by well-known major indexing services).

Additionally, for accepted papers at the Austrochip conference, the Institute of Electronics at Graz University of Technology together with the Austrochip 2023 Organizing Committee and the editors of the technical-scientific journal e&i elektrotechnik und informationstechnik (edited by the OVE Austrian Electrotechnical Association, published by Springer Vienna) cordially invite you to submit an extended paper for e&i's special issue on "Microelectronics: Austrochip 2023 and beyond".

Instructions for Authors:

Papers should not exceed 4 pages IEEE format (including bibliography and appendix). Instructions and additional details can be found here. Extended papers should be about 6-8 pages in length and submitted to the special issue "1.2024 Dr. Michalowska Forsyth". The e&i Journal submission guidelines can be found at here.

Conference Venue:

Austrochip 2023 is held in Graz on September 21st, 2023 at the Graz University of Technology. Registered participants will be provided with proceedings via a download link, lunch and coffee breaks.
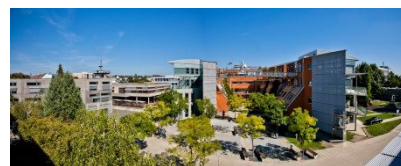
Contact/Organization:

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Web: https://austrochip.tugraz.at

Conference Venue

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Graz University of Technology

Important Dates

Submission Deadline
July 23, 2023 (extended)

Acceptance Notification
August 24, 2023

Workshop
September 20, 2023

Conference
September 21, 2023

Extended e&i Article Submission Deadline
September 30, 2023

Organizer